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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449

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ERT	AA	4,8	16,839	03/89	Landt							
1	AB	4,82	27,395	05/89	Anders et al.							
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	AF	4,86	52,160	08/89	Ekchian et al.							
	AG	4,8	70,419	09/89	Baldwin et al.						-	
	AH	4,88	38,591	12/89	Landt et al.				-			
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SICE			Solid State Circu	its, Vol. SC-1	2, No. 3, June 1977							
	AS		Mitsubishi Motors	Corporation V	Veb Page, 1995				•	•		
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683	AT		"Digital RF/ID Enl	nances GPS",	by John R. Tuttle, Proceedings of	the Second Annual V	/ireless Sym	posium	, рр.400	5-411,		
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	AC	5,175,774	12/92	Truax et al.		<u> </u>				
	AD	5,206,609	04/93	Mijuskovic						
	AE	5,218,343	06/93	Stobbe et al.		-				
	AF	5,231,273	07/93	Caswel		_				
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ATTY. DOCKET NO. MI40-325 U.S. DEPARTMENT OF COMMERCE SERIAL NO. **37 822** Form PTO-1449 PATENT AND TRADEMARK OFFICE LIST OF ART CITED BY APPLICANT APPLICANT (Use several sheets if necessary) James E. O'Toole et al. GROUP FILING DATE Filed Herewith U.S. PATENT DOCUMENTS *Examiner Document Date Name Class Subclass Filing Date If Appropriate Initial Number 5,311,186 05/94 Utsu et al. AA 5,323,150 06/94 Tuttle AB AC 5,355,513 10/94 Clarke et al. 11/94 Snodgrass et al. 5,365,551 AD 12/94 AE 5,374,930 Schuermann 02/95 ΑF 5,394,444 Silvey et al. AG 5,406,263 04/95 Tuttle 5,412,351 05/95 ΑH Nystrom et al. ΑI 5,420,757 05/95 Eberhardt et al. ΑJ 5,430,441 07/95 Bickley et al. 5,444,223 08/95 FOREIGN PATENT DOCUMENTS Document Date Country Class Subclass Translation Number Yes No AM ΑN AO ΑP OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) Donald O. Pederson, "Analog Integrated Circuits for Communication, Principles, Simulation and Design," Kluwer Academic Publishers, 1991, pp. 431-433. Barrie Gilbert, "A Precise Four-Quadrant Multiplier with Subnanosecond Response," IEEE Journal of Solid-State Circuits, 1968, AS pp. 365-373. ΑT DATE CONSIDERED :3-19-04 **EXAMINER** *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	AB	5,448,110	09/95	Tuttle et al.				_	
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	AD	5,448,772	09/95	Grandfield					
	AE	5,450,087	09/95	Hurta et al:		_			
	AF	5,461,385	10/95	Armstrong					
	AG	5,471,212	11/95	Sharpe et al.		_			
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	AI	5,489,546	02/96	Ahmad et al.		_	_		
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Form PTO-1449 U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. PATENT AND TRADEMARK OFFICE M140-325 LIST OF ART CITED BY APPLICANT APPLICANT James E. O'Toole et al. (Use several sheets if necessary) FILING DATE GROUP Filed Herewith U.S. PATENT DOCUMENTS Filing Date Date Class Subclass Document Name *Examiner Number If Appropriate Initial $\mathbf{A}\mathbf{A}$ 5,686,920 11/97 Hurta et al. ΑB 5,703,509 12/97 Hirata AC 5,719,550 02/98 Bloch et al. AD 5,721,678 02/98 Widl ΑÉ 5,721,783 02/98 Anderson ΑF 5,726,630 03/98 Marsh et al. AG 5,815,042 09/98 Chow et al. ΑH 5,901,349 05/99 Guegnaud et al. ΑI 4,746,830 05/88 Holland 11/88 ΑJ 4,786,903 Grindahl et al. ΑK 4,952,889 08/90 Irwin et al. FOREIGN PATENT DOCUMENTS Date Class Subclass Translation Document Country Number AL ΑM AN ΑO OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) AR AS ΑT 3-19-04 **EXAMINER** DATE CONSIDERED *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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		AE	5,103,156	04/07/92	Jones et al. Technolog	y Center 2600	320	35		
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U.S. DEPARTMENT OF COMMERCE SERIAL NO. Form PTO-1449 ATTY. DOCKET NO. PATENT AND TRADEMARK OFFICE M140-325 09/822,063 LIST OF ART CITED BY APPLICANT APPLICANT (Use several sheets if necessary) Micron Technology, Inc. GROUP FILING DATE U.S. PATENT DOCUMENTS Document Date Name Class Subclass Filing Date If Appropriate Number 257 471 5,780,916 7/98 ΑA Berger et al. AB 5,143,820 09/92 Kotecha et al. 430 314 4,478,881 AC 10/84 Bartur et al. RECEIVED AD JAN 1 0 200 ΑĖ Technology Center 2600 ΑF AG ΑH ΑI ΑJ FOREIGN PATENT DOCUMENTS Class Subclass Translation Document Date Country Number No AL AM AN ΑO OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) Fink & Christiansen, ELECTRONIC ENGINEER'S HANDBOOK, 3rd Edition, McGraw-Hill, 1989, pgs. 8-22, 8-23 and 8-40. Wolf, SILICON PROCESSING FOR THE VLSI ERA VOLUME 2: PROCESS INTEGRATION, 1986, Lattice Press, pgs. 65 and 195. DATE CONSIDERED **EXAMINER** *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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	18	LIST OF ART CITED (Use several sheets		CANT	APPLICANT James E. O'Toole et	al.				
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	AQ		OTHER REFER	RENCES (including Author, Title, Date, P	ertinent Pages. Etc.)	<u> </u>	<u> </u>			
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			not citation is	in conformance with MPEP 609; Draw	line through citation if	not in confo	ormance and not	considered.	Include	
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